



PATENT
Docket No.: 2207/8754
Assignee: INTEL CORPORATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Technology Center 2

APPLICANT : RONEN et al.
SERIAL NO. : 09/536,452
FILED : March 28, 2000
TITLE : CONFINING AN APPLICATION TO A 32-BIT ADDRESS
SPACE SUBSET IN A 64-BIT PROCESSOR
GROUP ART UNIT : 2183
EXAMINER : HUISMAN, DAVID J.

COMMISSIONER FOR PATENTS
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Alexandria, VA 22313-1450

LETTER TO OFFICIAL DRAFTSPERSON

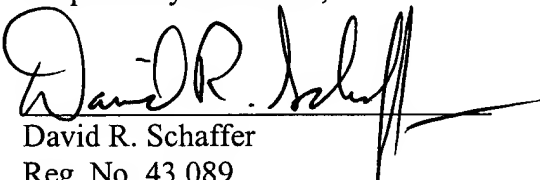
SIR:

Applicants respectfully request approval of the formal drawing submitted herewith incorporating the Examiner's requirement to label component 140 in FIG. 1 as a 64-bit address register.

The Office is hereby authorized to charge any additional fees or credit any overpayments under 37 C.F.R. § 1.16 or § 1.17 to Deposit Account No. 11-0600.

Respectfully submitted,

Dated: July 11, 2003

By: 
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